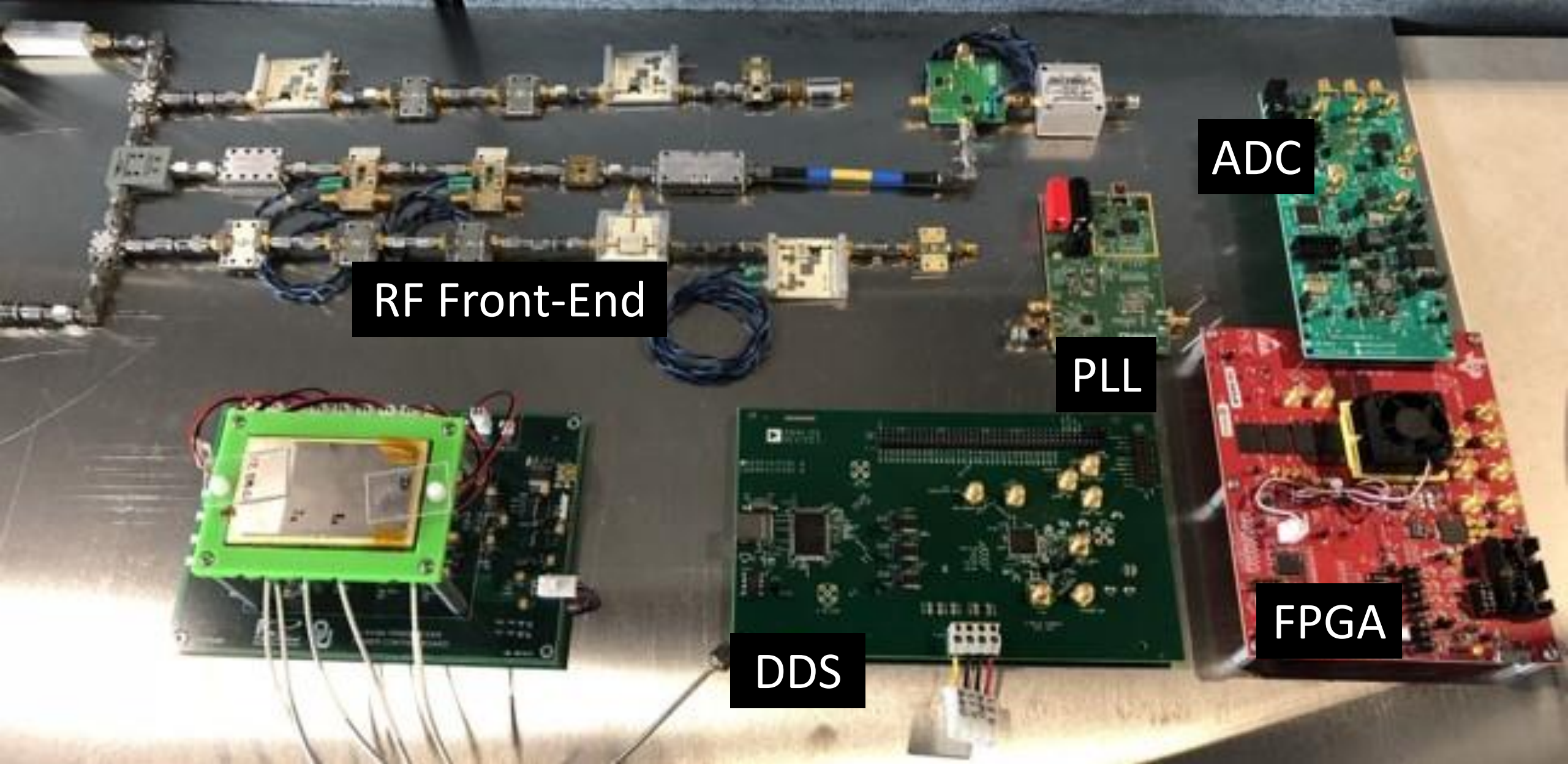




RF Front-End

ADC

PLL

DDS

FPGA